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To:	Examiner Thanh Y. Tran	From:	Fred G. Pruner, Jr.
Company:	U.S. Patent & Trademark Office	Date:	March 30, 2005
Fax:	(703) 872-9306	Pages:	2 (including coversheet)
Your Re:	09/811,063	Our Re:	ITL.0519US(P10729)

☐ Urgent ☐ For Review ☐ Please Comment ☐ Please Reply ☐ Confirm Receipt

MESSAGE:

Applicant: Joe A. Harrison et al.
Serial No.: 09/811,063
Filing Date: March 16, 2001
Title: Circuit Board and Slot Connector Assembly

1. Interview Summary

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicants:	Joe A. Harrison, et al.	§	Art Unit:	2822
Serial No.:	09/811,063	§	Examiner:	Thanh Y. Tran
Filed:	March 16, 2001	§	Assignee:	Intel Corporation
Title:	Circuit Board and Slot Connector Assembly	§	Docket No.	ITL.0519US (P10729)

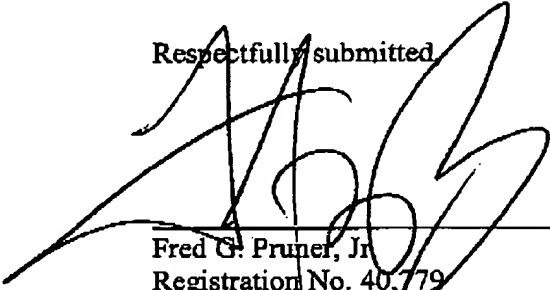
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

Dear Sir:

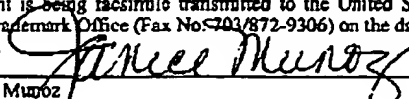
The undersigned and Examiner Tran discussed the application in a telephonic interview on March 29, 2005. In this discussion, Examiner Tran agreed to cancel, by Examiner's Amendment, the remaining rejected claims and mail out a Notice of Allowance. No specific prior art was discussed.

Respectfully submitted,

Date: March 30, 2005

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Date of Deposit:	March 30, 2005
I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 703/872-9306) on the date indicated above.	
	
Janice Munoz	